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MESSAGE

Re: Serial No. 10/656,434, filed September 5, 2003

1. Request For Reconsideration Of April 11, 2005 Final Office Action

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RESPONSE UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2826

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JUL 05 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hitoshi YAMAMOTO

Serial No.: 10/656,434

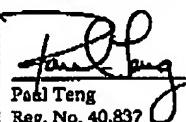
Group Art Unit: 2826

Date Filed: September 5, 2003

Examiner: Alexander O. Williams

For: SEMICONDUCTOR DEVICE RESPONSIVE TO DIFFERENT LEVELS OF INPUT
AND OUTPUT SIGNALS AND SIGNAL PROCESSING SYSTEM USING THE SAME
(as amended)

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facsimile to the USPTO and is being deposited this date with the U.S.
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Paul Teng
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July 5, 2005

Date

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**REQUEST FOR RECONSIDERATION OF
APRIL 11, 2005 FINAL OFFICE ACTION**

Sir:

This Request for Reconsideration is submitted in response to the final Office Action
dated April 11, 2005 in connection with the above-identified application.

A Listing of Claims section which begins on page 2 is provided for the convenience of
the Examiner.

Remarks begin on page 5 of this paper.

A graphical comparison of the semiconductor device of Akiyama and a semiconductor
device according to one exemplary embodiment of this application is attached hereto as
Exhibit A.